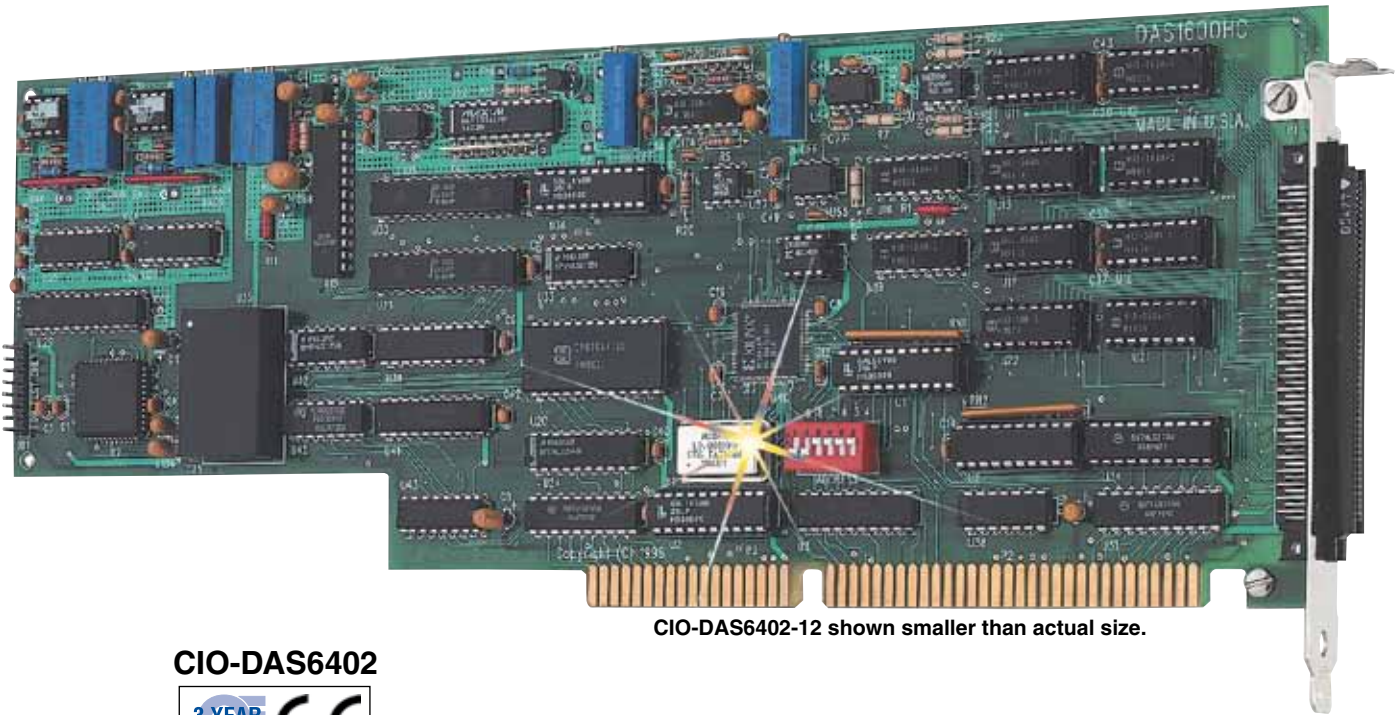


High Speed 64-Channel Analog Input Boards



CIO-DAS6402-12 shown smaller than actual size.

CIO-DAS6402



- ✓ 32 Differential / 64 Single-Ended Analog Inputs
- ✓ Models with 12 or 16-bit A/D Resolution
- ✓ 100KHz Sample Rate
- ✓ Dual 12 or 16-Bit Analog Outputs
- ✓ 1024 Sample FIFO
- ✓ 16-bits Digital I/O
- ✓ 3 Counter Timers

The CIO-DAS6402 multifunction analog and digital I/O boards set the new standard for high channel count, high speed data acquisition. Installed in any ISA-bus compatible personal computer, the CIO-DAS6402 turns your personal computer into a high speed data acquisition and control station suitable for laboratory data collection, instrumentation, production test, or industrial monitoring.

FIFO Provides Full Data Rate Under Windows

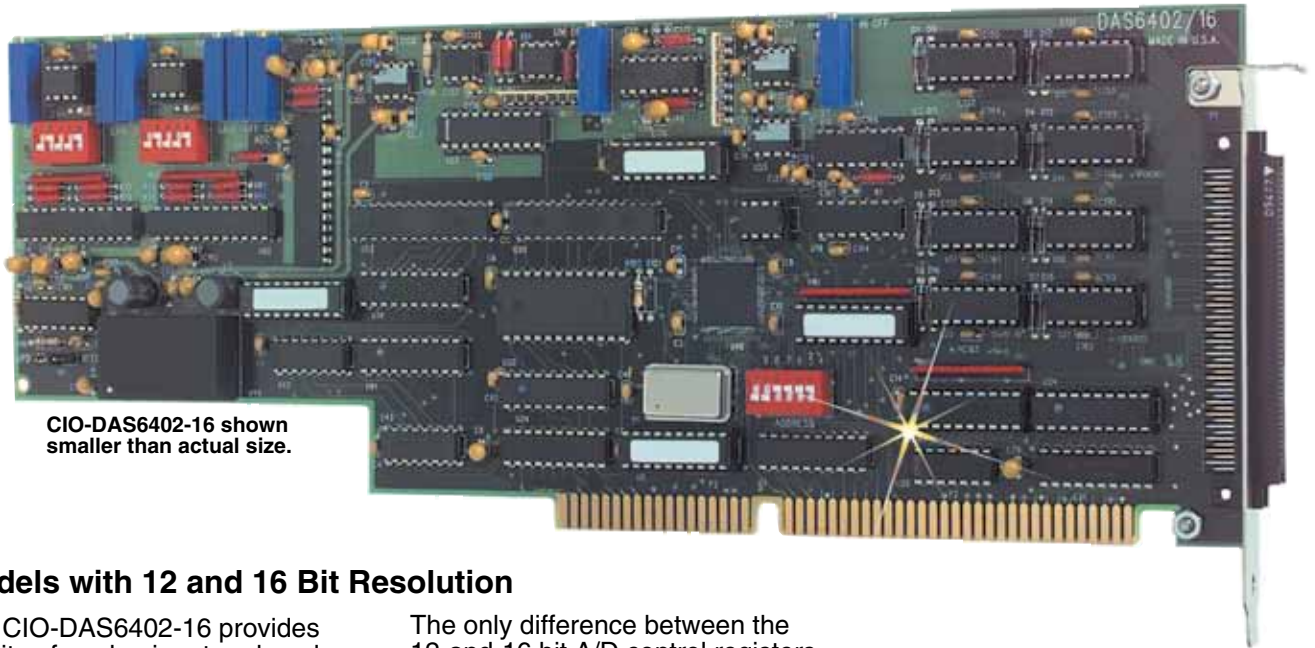
The on-board 1024 sample FIFO buffer collects the results of A/D conversions and stores them until the computer's CPU is able to transfer the data into PC memory. The FIFO buffer allows the PC to store up the A/D transfer requests, then service the requests in batches. The FIFO is necessary to obtain the full data acquisition rates under multitasking operating systems like Windows.

Connector

All I/O signals are brought through a 100-pin high-density connector. Field wiring is greatly simplified by using the optional C100-FF2 cable and CIO-TERM100 screw terminal board. The pinout of the CIO-DAS6402 is shown at right.

LLGND	1	●	51	LLGND
N0+	2	●	52	N16+
N0-N32+	3	●	53	N16-N48+
N1+	4	●	54	N17+
N1-N33+	5	●	55	N17-N49+
N2+	6	●	56	N18+
N2-N34+	7	●	57	N18-N50+
N3+	8	●	58	N19+
N3-N35+	9	●	59	N19-N51+
N4+	10	●	60	N20+
N4-N36+	11	●	61	N20-N52+
N5+	12	●	62	N21+
N5-N37+	13	●	63	N21-N53+
N6+	14	●	64	N22+
N6-N38+	15	●	65	N22-N54+
N7+	16	●	66	N23+
N7-N39+	17	●	67	N23-N55+
LLGND	18	●	68	LLGND
N8+	19	●	69	N24+
N8-N40+	20	●	70	N24-N56+
N9+	21	●	71	N25+
N9-N41+	22	●	72	N25-N57+
N10+	23	●	73	N26+
N10-N42+	24	●	74	N26-N58+
N11+	25	●	75	N27+
N11-N43+	26	●	76	N27-N59+
N12+	27	●	77	N28+
N12-N44+	28	●	78	N28-N60+
N13+	29	●	79	N29+
N13-N45+	30	●	80	N29-N61+
N14+	31	●	81	N30+
N14-N46+	32	●	82	N30-N62+
N15+	33	●	83	N31+
N15-N47+	34	●	84	N31-N63+
GROUND FOR DAC0	35	●	85	DOU T0
DAC0 OUT/UT	36	●	86	DOU T1
GROUND FOR DAC1	37	●	87	DOU T2
DAC1 OUT/UT	38	●	88	DOU T3
CTRO CLK IN	39	●	89	CHASSIS GND
DN2/CTRO GATE	40	●	90	+12V SUPPL Y OUT
COUNTS R0 OUT/UT	41	●	91	CHASSIS GND
DN0/AD PACER IN	42	●	92	-12V SUPPL Y OUT
DN1/AD GATE/AD TRIG	43	●	93	DN 6
DN 3	44	●	94	DN 7
DN 4	45	●	95	DOU T4
DN 5	46	●	96	DOU T5
-5V REF OUT	47	●	97	DOU T6
+5V SUPPL Y OUT	48	●	98	DOU T7
SSH OUT	49	●	99	EXTERNAL INTERRUPT IN
CHASSIS GND	50	●	100	CHASSIS GND

CIO-DAS6402 Signal Connector



CIO-DAS6402-16 shown smaller than actual size.

Models with 12 and 16 Bit Resolution

The CIO-DAS6402-16 provides 16 bits of analog input and analog output resolution (1 part in 65,536) while the CIO-DAS6402-12 provides 12-bit resolution (1 part in 4096) for its analog inputs and outputs.

The only difference between the 12-and-16 bit A/D control registers is the A/D least significant byte data register. Shown below are the A/D data registers for the CIO-DAS-6402-12 and CIO-DAS-6402-16. The 16-bit board simply has useful data in the 4 least significant bits (instead of 0).

12-Bit Board A/D data format

D15	D14	D13 . . .	D5	D4	D3	D2	D1	D0
A/D11	A/D10	A/D9 . . .	A/D1	A/D0	0	0	0	0

16-Bit Board A/D data format

D15	D14	D13 . . .	D5	D4	D3	D2	D1	D0
A/D15	A/D14	A/D13 . . .	A/D5	A/D4	A/D3	A/D2	A/D1	A/D0

Analog Input Ranges

All A/D range selection on the CIO-DAS6402 is selected via software. The D/A range on the CIO-DAS6402/12 is also set via software while the output range of

the CIO-DAS6402/16 is set by DIP switches on the board. The ranges and resolutions available on the CIO-DAS6402 boards are shown below.

Bipolar Range	12-Bit Resolution	16-Bit Resolution	Unipolar Range	12-Bit Resolution	16-Bit Resolution
±10 V	4.88 mV	305 µV	0 - 10 V	2.44 mV	153 µV
±5 V	2.44 mV	153 µV	0 - 5 V	1.22 mV	76.3 µV
±2.5 V	1.22 mV	76.3 µV	0 - 2.5 V	0.61 mV	38.1 µV
±1.25 V	0.61 mV	38.1 µV	0 - 1.25 V	0.305 mV	19.1 µV

Minimizing Channel to Channel Skew

This is also the format difference when writing to the D/A registers.

All of the channels on the CIO-DAS6402 are multiplexed into a single A/D converter. Since there is only one A/D converter on the

board, a channel to channel skew (delay) occurs when scanning multiple channels. With many A/D boards, the time skew is equal to the sample rate, so a 1 KHz sample rate would produce a 1 millisecond skew time.

The CIO-DAS6402 features an enhanced triggering mode called the burst mode. In the burst mode the A/D converter is run at its maximum rate for the entire multi-channel scan, thus reducing the channel to channel skew time to the maximum A/D rate which is 4 µS for the 12-bit board and 10µS for the 16-bit board.

Software

The CIO-DAS6402 includes a complete test and calibration program. The program provides a step-by-step procedure for installing and configuring the card. It also creates a configuration file used by the optional Universal Library.

The Universal Library is a set of I/O libraries and drivers for those users creating their own custom programs. The Universal Library is compatible with most Windows based languages and supports the entire CIO family of boards. The Library includes an extensive set of programming examples written in Visual Basic and C.

An optional driver for LabView is also available.

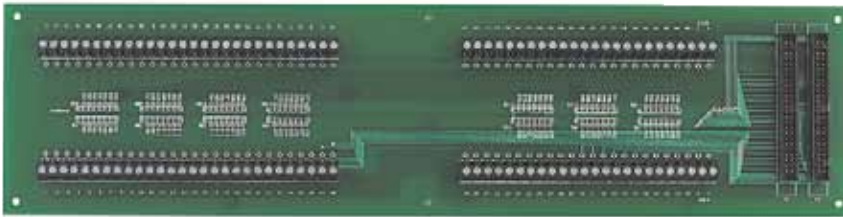
The CIO-DAS6400 is also compatible with many off-the-shelf programs including Labtech, DASyLab and SnapMaster.

Specifications

(Typical for 25°C unless otherwise specified.)

ANALOG INPUTS

	CIO-DAS-6402-16	CIO-DAS-6402-12
A/D Resolution	16 bits	12 bits
A/D Conversion Time	5 μS	3 μS
Throughput	100KHz min	333 KHz min
Integral Linearity error	±2 LSB max	±0.5 LSB max
Gain Drift (A/D specs)	±7 ppm/°C, all ranges	±6 ppm/°C, all ranges
Zero Drift (A/D specs)	±2 ppm/°C, all ranges	±1 ppm/°C, all ranges
Input Leakage Current	200 nA	
Input Impedance	10 MΩ	
Absolute Maximum Input Voltage	±15 V	
A/D Triggering Modes	Edge or level, programmable polarity unlimited pre and post trigger samples	



CIO-TERMINAL shown smaller than actual size.

ANALOG OUTPUTS

	CIO-DAS-6402-16	CIO-DAS-6402-12
D/A Resolution	16 bits	12 bits
Number of Channels	2	
Voltage Ranges	±2.5V, ±5V, ±10V, 0-2.5 V, 0-5 V, 0-10 V switch selectable	±5, ±10, 0-5, 0-10 software selectable
Differential Linearity	±2 LSB	±1 LSB
Integral Linearity	±2 LSB	±1 LSB
Gain Drift	±15 ppm/°C	
Bipolar Offset Drift	±5 ppm/°C	
Unipolar Offset Drift	±3 ppm/°C	
Settling Time (20V step)	19 μS max	8 μS max
Slew Rate	2.8 V/μS typ	4 V/μS typ
Current Drive	±5 mA min	±2 mA min
Short Circuit Protection	40 mA Continuous	25 mA Continuous
Output Impedance	0.1 Ω max	

To Order

Model Number	Description
CIO-DAS6402-16	64-channel, 16-bit analog I/O board
CIO-DAS6402-12	64-channel, 12-bit analog I/O board

Each CIO-DAS6400 includes a user's manual and test and calibration software.

OMEGACARESM extended warranty is available for models shown on this page.

Ask your sales representative for full details when placing order.

Ordering Example: CIO-DAS6402-16 board, CIO-TERM100 terminal board, OMEGACARESM 1-year extended warranty for CIO-DAS6402-16 (adds 1 year to standard 3-year warranty), and C100-FF-2 cable.

Model No.	Description
CIO-TERM100	100 terminal screw terminal adapter board, requires cable
C100FF-2	100 conductor cable



The CIO-DAS6402 includes Instacal calibration and Testing software



OMEGACARESM extended warranty program is available for models shown on this page. Ask your sales representative for full details when placing an order. OMEGACARESM covers parts, labor and equivalent loaners.

DIGITAL INPUT / OUTPUT

Digital Type: Output - 74LS244, Input - 74LS273

Configuration: Two dedicated ports, 8 input and 8 output

Output High: 2.7 volts @ -0.4mA min

Output Low: 0.4 volts @ 8 mA min

Input High: 2.0 volts min, 7 volts absolute max

Input Low: 0.8 volts max, -0.5 volts absolute min

COUNTER

Counter Type: 82C54

Configuration: 3 down counters, 16-bits each

ENVIRONMENTAL

Operating Temperature Range: 0 to 70°C

Storage Temperature Range: -40 to 100°C

Humidity: 0 to 90% non-condensing

POWER CONSUMPTION

Icc: Operating (CIO-DAS6402-16): 1.17A typical, 1.67A max

Icc: Operating (CIO-DAS6402-12): 1.05A typical, 1.6A max